

## 001 The Temporal Logic Of Reactive And Concurrent Systems Specification

This book constitutes the thoroughly refereed post-workshop proceedings of the 10th International Workshop on Rewriting Logic and its Applications, WRLA 2014, held as a satellite event of ETAPS 2014, in Grenoble, France, in March 2014. The 13 revised full papers presented together with 3 invited papers were carefully reviewed and selected from 21 submissions. The papers address a great diversity of topics in the fields of foundations and models of RL: languages based on RL: RL as a logical framework: RL as a semantic framework: use of RL to provide rigorous support for model-based software engineering: formalisms related to RL: verification techniques for RL specifications: comparisons of RL with existing formalisms having analogous aims: application of RL to specification and analysis of distributed systems and physical systems.

The aim of this book is to provide an introduction to probability logic-based formalization of uncertain reasoning. The authors' primary interest is mathematical techniques for infinitary probability logics used to obtain results about prof-theoretical and model-theoretical issues such as axiomatizations, completeness, compactness, and decidability, including solutions of some problems

from the literature. An extensive bibliography is provided to point to related work, and this book may serve as a basis for further research projects, as a reference for researchers using probability logic, and also as a textbook for graduate courses in logic.

"This book explores some of the most recent developments in robotic motion, artificial intelligence, and human-machine interaction, providing insight into a wide variety of applications and functional areas"--Provided by publisher.

Reactive systems are computing systems which are interactive, such as real-time systems, operating systems, concurrent systems, control systems, etc. They are among the most difficult computing systems to program. Temporal logic is a formal tool/language which yields excellent results in specifying reactive systems. This volume, the first of two, subtitled Specification, has a self-

contained introduction to temporal logic and, more important, an introduction to the computational model for reactive programs, developed by Zohar Manna and Amir Pnueli of Stanford University and the Weizmann Institute of Science, Israel, respectively.

10th International Workshop, SOFL+MSVL 2020, Singapore, March 1, 2021, Revised Selected Papers

Runtime Verification

Verifying Concurrent Processes Using Temporal Logic

The Complete Industry Cycle

Concepts, Methodologies, Tools, and Applications

Practical Aspects of Declarative Languages

*The refereed proceedings of the 19th International Conference on Automated Deduction, CADE 2003, held in Miami Beach, FL, USA in July 2003. The 29 revised full papers and 7 system description papers presented together with an invited paper and 3 abstracts of invited talks were carefully reviewed and selected from 83 submissions. All current aspects of automated deduction are discussed, ranging from theoretical and methodological issues to the presentation of new theorem provers and systems.*

*This proceedings volume examines parameterized systems, model checking, applications, static analysis, concurrent/distributed systems, symbolic execution, abstraction, interpolation, trust, and reputation.*

*"The TIME symposium is relevant, among others, to the following topics belonging to the IEEE CS FOI list F 4 1 k Temporal Logic F 4 3 b Classes defined by grammars or automata G 4 i Verification H 2 4 mTemporal databases I 1 2 Algorithms "*

*The Temporal Logic of Reactive and Concurrent SystemsSpecificationsSpringer Science & Business Media*

*A Stuttering-robust Temporal Logic with "next" But Without "previous".*

Safety

12th International Conference, LPAR 2005, Montego Bay, Jamaica, December 2-6, 2005, Proceedings

Comprehensive Functional Verification

Handbook of Modal Logic

Third International Symposium, PADL 2001 Las Vegas, Nevada, March 11-12, 2001 Proceedings

Handbook of Temporal Reasoning in Artificial Intelligence

**Embedded systems are nearly ubiquitous, and books on individual topics or components of embedded systems are equally abundant. Unfortunately, for those designers who thirst for knowledge of the big picture of embedded systems there is not a drop to drink. Until now. The Embedded Systems Handbook is an oasis of information, offering a mix of basic a**

**Proceedings**

**This collection represents the primary reference work for researchers and students in the area of Temporal Reasoning in Artificial Intelligence. Temporal reasoning has a vital role to play in many areas, particularly Artificial Intelligence. Yet, until now, there has been no single volume collecting together the breadth of work in this area. This collection brings together the leading researchers in a range of relevant areas and provides an coherent description of the breadth of activity concerning temporal reasoning in the field of Artificial Intelligence. Key Features:**

**- Broad range: foundations; techniques and applications - Leading researchers around the world have written the chapters - Covers many vital applications - Source book for Artificial Intelligence, temporal reasoning - Approaches provide foundation for many future software systems - Broad range: foundations; techniques and applications - Leading researchers around the world have written the chapters -**

**Covers many vital applications - Source book for Artificial Intelligence, temporal reasoning - Approaches provide foundation for many future software systems**

**This book provides an invaluable overview of the reach of logic. It provides reference to some of the most important, well-established results in logic, while at the same time offering insight into the latest research issues in the area. It also has a balance of theory and practice, containing essays in the areas of Modal Logic, Intuitionistic Logic, Logic and Language, Non-monotonic Logic and Logic Programming, Temporal Logic, Logic and Learning, Combination of Logics, Practical Reasoning, Logic and Artificial Intelligence, Abduction, Theorem Proving, and Goal-Directed Reasoning. It will be invaluable reading for researchers and graduate students in Logic and Computer Science, and a fabulous source of inspiration for research students in search of a topic for a PhD in logic and theoretical computer science.**

**Advances in Computer Science - ASIAN 2006. Secure Software and Related Issues**

**Automated Deduction - CADE-19**

**Decision and Game Theory for Security**

**Temporal Logic of Reactive and Concurrent Systems**

**Verification, Model Checking, and Abstract Interpretation**

**18th International Conference, LPAR-18, Merida, Venezuela, March 11-15, 2012, Proceedings**

**Methods and Best Practices**

*This book constitutes the refereed workshop proceedings of the 10th International Workshop on Structured Object-Oriented Formal Language and Method, SOFL+MSVL 2020, held in Singapore, in March 2021. The 13 revised full papers included in the volume were carefully reviewed and selected from 24 submissions. They are organized in the following topical sections: modeling and specification; model checking; specification and verification; and testing and formal verification.Due to the Corona pandemic this event was held virtually.*

*This book constitutes the refereed proceedings of the 5th International Conference on Runtime Verification, RV 2014, held in Toronto, ON, Canada in September 2014. The 28 revised full papers presented together with 2 tool papers, and 8short papers were carefully reviewed and selected from 70 submissions. The scope of the conference was on following topics: monitoring and trace slicing, runtime verification of distributed and concurrent systems, runtime verification of real-time and embedded systems, testing and bug finding, and inference and learning.*

*Self-concept and coping behaviour are important aspects of development in adolescence. Despite their developmental significance, however, the two areas have rarely been considered in relation to each other. This book is the first in which the two areas are brought together: it suggests that this interaction can open the way to new possibilities for further research and to new implications for applied work with adolescents. Two separate chapters review research carried out in each of the areas. These are followed by a series of more empirically focussed chapters in which issues such as changes in relationship patterns, difficult school situations, leaving school, use of leisure, anxiety and suicidal behaviour are examined in the context of self-concept and coping. The final chapter seeks to identify some of the central themes emerging from this work and discusses possible research and applied implications.*

*Covers central topics in information systems modeling and architectures. Includes the latest developments in information systems modeling, methods, and best practices.*

*10th International Workshop, WRLA 2014, Held as a Satellite Event of ETAPS, Grenoble, France, April 5-6, 2014, Revised Selected Papers*

*A Portrait of State-of-the-Art Research at the Technical University of Lisbon*

*5th International Conference, RV 2014, Toronto, ON, Canada, September 22-25, 2014, Proceedings*

*Spatio-Temporal Stream Reasoning with Adaptive State Stream Generation*

*Principles of Knowledge Representation and Reasoning*

*Structural Object-Oriented Formal Language and Method*

*10th International Conference, GameSec 2019, Stockholm, Sweden, October 30 – November 1, 2019, Proceedings*

This book constitutes the refereed proceedings of the 10th International Conference on Decision and Game Theory for Security, GameSec 2019, held in Stockholm, Sweden, in October 2019.The 21 full papers presented together with 11 short papers were carefully reviewed and selected from 47 submissions.The papers focus on protection of heterogeneous, large-scale and dynamic cyber-physical systems as well as managing security risks faced by critical infrastructures through rigorous and practically-relevant analytical methods.

This volume constitutes the proceedings of the 14th International Conference on Combinatorial Optimization and Applications, COCOA 2020, held in Dallas, TX, USA, in December 2020. The 55 full papers presented in this volume were carefully reviewed and selected from 104 submissions. The papers are grouped into the following topics: Approximation Algorithms; Scheduling; Network Optimization; Complexity and Logic; Search, Facility and Graphs; Geometric Problem; Sensors, Vehicles and Graphs; and Graph Problems. Due to the Corona pandemic this event was held virtually.

A lot of today's data is generated incrementally over time by a large variety of producers. This data ranges from quantitative sensor observations produced by robot systems to complex unstructured human-generated texts on social media. With data being so abundant, making sense of these streams of data through reasoning is challenging. Reasoning over streams is particularly relevant for autonomous robotic systems that operate in a physical environment. They commonly observe this environment through incremental observations, gradually refining information about their surroundings. This makes robust management of streaming data and its refinement an important problem. Many contemporary approaches to stream reasoning focus on the issue of querying data streams in order to generate higher-level information by relying on well-known probabilistic and logic-based reasoning techniques, which rarely consider the provenance of their symbolic interpretations. In this thesis, we integrate techniques for logic-based spatio-temporal stream reasoning with the adaptive generation of the state streams needed to do the reasoning over. This combination deals with both the challenge of reasoning over streaming data and the problem of robustly managing streaming data and its refinement. The main contributions of this thesis are (1) a logic-based spatio-temporal reasoning technique that combines temporal reasoning with qualitative spatial reasoning; (2) an adaptive reconfiguration procedure for generating and maintaining a data stream required to perform spatio-temporal stream reasoning over; and (3) integration of these two techniques into a stream reasoning framework. The proposed spatio-temporal stream reasoning technique is able to reason with intertemporal spatial relations by leveraging landmarks. Adaptive state stream generation allows the framework to adapt in situations in which the set of available streaming resources changes. Management of streaming resources is formalised in the DyKnow model, which introduces a configuration life-cycle to adaptively generate state streams. The DyKnow-ROS stream reasoning framework is a concrete realization of this model that extends the Robot Operating System (ROS). DyKnow-ROS has been deployed on the SoftBank Robotics NAO platform to demonstrate the system's capabilities in the context of a case study on run-time adaptive reconfiguration. The results show that the proposed system - by combining reasoning over and reasoning about streams - can robustly perform spatio-temporal stream reasoning, even when the availability of streaming resources changes.

This book constitutes the refereed proceedings of the 13th International Conference on Tools and Algorithms for the Construction and Analysis of Systems, TACAS 2007, held in Braga, Portugal. Coverage includes software verification, probabilistic model checking and markov chains, automata-based model checking, security, software and hardware verification, decision procedures and theorem provers, as well as infinite-state systems.

Specifications

FM'99 - Formal Methods

Logic For Programming, Artificial Intelligence, and Reasoning

11th Asian Computing Science Conference, Tokyo, Japan, December 6-8, 2006, Revised Selected Papers

World Congress on Formal Methods in the Development of Computing Systems, Toulouse, France, September 20-24, 1999 Proceedings, Volume II

Robotics: Concepts, Methodologies, Tools, and Applications

Temporal Information Processing Technology and Its Applications

**The Handbook of Modal Logic contains 20 articles, which collectively introduce contemporary modal logic, survey current research, and indicate the way in which the field is developing. The articles survey the field from a wide variety of perspectives: the underlying theory is explored in depth, modern computational approaches are treated, and six major applications areas of modal logic (in Mathematics, Computer Science, Artificial Intelligence, Linguistics, Game Theory, and Philosophy) are surveyed. The book contains both well-written expository articles, suitable for beginners approaching the subject for the first time, and advanced articles, which will help those already familiar with the field to deepen their expertise. Please visit:**

**http://people.uleth.ca/~woods/RedSeriesPromo\_WP/PubSIPR.html - Compact modal logic reference - Computational approaches fully discussed - Contemporary applications of modal logic covered in depth**

This book constitutes the proceedings of the 18th International Conference on Logic for Programming, Artificial Intelligence, and Reasoning, LPAR-18, held in Merida, Venezuela, in March 2012. The 25 regular papers and 6 tool descriptions and experimental papers presented were carefully reviewed and selected from 74 submissions. The series of International Conferences on Logic for Programming, Artificial Intelligence and Reasoning (LPAR) is a forum where, year after year, some of the most renowned researchers in the areas of logic, automated reasoning, computational logic, programming languages and their applications come to present cutting-edge results, to discuss advances in these fields, and to exchange ideas in a scientifically emerging part of the world. This book is about the verification of reactive systems. A reactive system is a system that maintains an ongoing interaction with its environment, as opposed to computing some final value on termination. The family of reactive systems includes many classes of programs whose correct and reliable construction is considered to be particularly challenging, including concurrent programs, embedded and process control programs, and operating systems. Typical examples of such systems are an air traffic control system, programs controlling mechanical devices such as a train, or perpetually ongoing processes such as a nuclear reactor. With the expanding use of computers in safety-critical areas, where failure is potentially disastrous, correctness is crucial. This has led to the introduction of formal verification techniques, which give both users and designers of software and hardware systems greater confidence that the systems they build meet the desired specifications. Framework The approach promoted in this book is based on the use of temporal logic for specifying properties of reactive systems, and develops an extensive verification methodology for proving that a system meets its temporal specification. Reactive programs must be specified in terms of their ongoing behavior, and temporal logic provides an expressive and natural language for specifying this behavior. Our framework for specifying and verifying temporal properties of reactive systems is based on the following four components: 1. A computational model to describe the behavior of reactive systems. The model adopted in this book is that of a Fair Transition System (FTS).

Temporal logic is two-valued: formulas are interpreted as either true or false. When applied to the analysis of stochastic systems, or systems with imprecise formal models, temporal logic is therefore fragile: even small changes in the model can lead to opposite truth values for a specification. We present a generalization of the branching-time logic C(sub TL) which achieves robustness with respect to model perturbations by giving a quantitative interpretation to predicates and logical operators, and by discounting the importance of events according to how late they occur. In every state, the value of a formula is a real number in the interval [0,1], where 1 corresponds to truth and 0 to falsehood. The boolean operators and or are replaced by min and max, the path quantifiers for all possible futures and some possible futures determine sup and inf over all paths from a given state, and the temporal operators for always and eventually specify sup and inf over a given path; a new operator averages all values along a path. Furthermore, all path operators are discounted by a parameter that can be chosen to give more weight to states that are closer to the beginning of the path. We interpret the resulting logic D(sub C(SUB TL)) over transition systems, Markov chains, and Markov decision processes. We present two semantics for D(sub C(SUB TL)): a path semantics, inspired by the standard interpretation of state and path formulas in C(sub TL), and a fixpoint semantics, inspired by the micro-calculus evaluation of C(sub TL) formulas. We show that, while these semantics coincide for C(sub TL), they differ for D(sub CTL), and we provide model-checking algorithms for both semantics.

Combinatorial Optimization and Applications

Temporal Verification of Reactive Systems

Embedded Systems Handbook

**The Second International Conference on Computers and Applications, Beijing (Peking), People's Republic of China, June 23-27, 1987**

**Temporal-Logic Theorem Proving**

**AIJ, April 8-10, 1987, Proceedings**

**Multi-valued Temporal Logic Based Reasoning System with Applications to Decision Support in Intelligent Environments**

Abstract: "The logic TLR, introduced in [1], is a temporal logic that is insensitive to stuttering but still possesses a well-defined next operator. Due to the combination of these two features, it presents an attractive foundation for studying refinement between reactive programs in a TL framework. A drawback of TLR is that completeness is achieved at the price of introducing the previous operator, as the only past operator, and is otherwise not used for specification or verification. This drawback is corrected in this paper which presents a pure future version of the logic, called FTLR, eliminating the previous operator. An alternative axiomatic system, not dependent on the removed operator, is presented and shown to be complete."

This work deals with decision-making problems with uncertain information in dynamic environment. It develops a new logic system, multi-valued temporal propositional logic, combining multi-valued logic and linear temporal logic. This new logic allows uncertain information to be represented with either a numerical truth value in the [0,1] interval or a linguistic value, and uses these values with both states and same-time and next-time rules. Multi-valued temporal propositional logic, a generic logic system, provides a simple calculus for analysing uncertain information with Lukasiewicz implication algebra. It introduces uncertainty and temporality into rules. Soundness and completeness theorems provide a theoretical foundation for the reasoning system. Based on the new logic, forward and backward reasoning algorithms are proposed, which offers simulation/prediction and query answering functions. The reasoning system, based on the forward and backward reasoning algorithms was programmed in Pro log. Three scenarios were then used to evaluate this reasoning system. The results of the evaluations showed that the proposed reasoning system could return reliable and reasonable feedback to users on their input. Furthermore, users are able to perfect their knowledge bases according to the feedback from the reasoning system on their input.

The book compares the reasoning system and dynamic Bayesian networks with a simple scenario with uncertain information in a certain temporal environment was undertaken. Within this comparison, an analysis of advantages and disadvantages between the proposed reasoning system and dynamic - Bayesian networks has been provided.

Running to more than 368 pages, and complete with online files and updates, this book constitutes the thoroughly refereed post-proceedings of the 11th Asian Computing Science Conference, ASIAN 2006, held in Tokyo, Japan. The 17 revised full papers and 8 revised short papers presented together with 1 invited paper were carefully selected during two rounds of reviewing from 115 submissions. The papers cover theory, practice, applications, and experiences related to secure software.

"Temporal Information Processing Technology and Its Applications" systematically studies temporal information processing technology and its applications. The book covers following subjects: 1) time model, calculus and logic; 2) temporal data models, semantics of temporal variable 'now' temporal database concepts; 3) temporal query language, a typical temporal database management system: TempDB; 4) temporal extension on XML, workflow and knowledge base; and, 5) implementation patterns of temporal applications, a typical example of temporal application. The book is intended for researchers, practitioners and graduate students of databases, data/knowledge management and temporal information processing. Dr. Yong Tang is a professor at the Computer School, South China Normal University, China.

Probabilità Logics

Temporal Logic

19th International Conference on Automated Deduction Miami Beach, FL, USA, July 28 - August 2, 2003, Proceedings

2013 20th International Symposium on Temporal Representation and Reasoning (TIME)

Third International Workshop, VMCAI 2002, Venice, Italy, January 21-22, 2002, Revised Papers

Third International Conference CAISE '91, Trondheim, Norway, May 13-15, 1991

**Probability-Based Formalization of Uncertain Reasoning**

*This book constitutes the thoroughly refereed post-proceedings of the Third International Workshop on Verification, Model Checking, and Abstract Interpretation, VMCAI 2002, held in Venice, Italy in January 2002. The 22 revised full papers presented were carefully reviewed and selected from 41 submissions. The papers are organized in topical sections on security and protocols, timed systems and games, static analysis, optimization, types and verification, and temporal logics and systems.*

*This book celebrates the 75th anniversary of The Technical University of Lisbon (UTL). It provides a compelling picture of current state-of-art research at UTL. It contains the edited version of the invited lectures from a two day Symposium and brings together a comprehensive summary of high quality research contributions across basic and applied sciences. A broad spectrum of topics is covered reflecting UTL's worldwide recognition.*

*This book constitutes the refereed proceedings of the Third International Symposium on Practical Aspects of Declarative Programming, PADL 2001, held in Las Vegas, Nevada, USA in March 2001. The 23 revised full papers presented were carefully reviewed and selected from a total of 40 submissions. Among the topics covered are Mu-calculus, specification languages, Java, Internet programming, VRML, security protocols, database security, authentication protocols, Prolog programming, implementation, constraint programming, visual tracking, and model checking.*

*One of the biggest challenges in chip and system design is determining whether the hardware works correctly. That is the job of functional verification engineers and they are the audience for this comprehensive text from three top industry professionals. As designs increase in complexity, so has the value of verification engineers within the hardware design team. In fact, the need for skilled verification engineers has grown dramatically--functional verification now consumes between 40 and 70% of a project's labor, and about half its cost. Currently there are very few books on verification for engineers, and none that cover the subject as comprehensively as this text. A key strength of this book is that it describes the entire verification cycle and details each stage. The organization of the book follows the cycle, demonstrating how functional verification engages all aspects of the overall design effort and how individual cycle stages relate to the larger design process. Throughout the text, the authors leverage their 35 plus years experience in functional verification, providing examples and case studies, and focusing on the skills, methods, and tools needed to complete each verification task. Comprehensive overview of the complete verification cycle Combines industry experience with a strong emphasis on functional verification fundamentals Includes real-world case studies*

*13th International Conference, TACAS 2007 Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2007 Braga, Portugal, March 24 - April 1, 2007 Proceedings*

Temporal Logic of Programs

Temporal Logic in Specification

Rewriting Logic and Its Applications

Advanced Information Systems Engineering

14th International Conference, TACAS 2008, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2008, Budapest, Hungary, March 29-April 6, 2008, Proceedings

Tools and Algorithms for the Construction and Analysis of Systems

This book constitutes the refereed proceedings of the 12th International Conference on Logic for Programming, Artificial Intelligence, and Reasoning, LPAR 2005, held in Montego Bay, Jamaica in December 2005. The 46 revised full papers presented together with abstracts of 3 invited talks were carefully reviewed and selected from 108 full paper submissions. The papers address all current issues in logic programming, logic-based program manipulation, formal method, automated reasoning, and various kinds of AI logics.

Formal methods are coming of age. Mathematical techniques and tools are now regarded as an important part of the development process in a wide range of industrial and governmental organisations. A transfer of technology into the mainstream of systems development is slowly, but surely, taking place. FM'99, the First World Congress on Formal Methods in the Development of Computing Systems, is a result, and a measure, of this new-found maturity. It brings an impressive array of industrial and applications-oriented papers that show how formal methods have been used to real problems. These proceedings are a record of the technical symposium off'M'99 also- side the papers describing applications of formal methods, you will nd technical reports, papers, and abstracts detailing new advances in formal techniques, from mathematical foundations to practical tools. The World Congress is the successor to the four Formal Methods Europe Symposia, which in turn succeeded the four VDM Europe Symposia.

This symposium was held in conjunction with the 1999 ACM Symposium on Foundations of Software Technology and Theory of Computing. The World Congress is the successor to the four Formal Methods Europe Symposia, which in turn succeeded the four VDM Europe Symposia. This symposium was held in conjunction with the 1999 ACM Symposium on Foundations of Software Technology and Theory of Computing. The World Congress is the successor to the four Formal Methods Europe Symposia, which in turn succeeded the four VDM Europe Symposia. This symposium was held in conjunction with the 1999 ACM Symposium on Foundations of Software Technology and Theory of Computing. The World Congress is the successor to the four Formal Methods Europe Symposia, which in turn succeeded the four VDM Europe Symposia.

The papers were submitted covering a wide variety of formal methods and application areas. The programme committee reflects the Congress's international nature, with a membership of 84 leading researchers from 38 different countries. The committee was divided into 19 tracks, each with its own chair to oversee the reviewing process. Our collective task was a difficult one: there were 259 high-quality submissions from 35 different countries.

BRICS Dissertation Series

Innovations in Information Systems Modeling: Methods and Best Practices

We Will Show Them! Essays in Honour of Dov Gabbay

14th International Conference, COCOA 2020, Dallas, TX, USA, December 11-13, 2020, Proceedings

Reasoning in the Temporal Logic of Actions. 96/1 (August 1996)

Proceedings of the Seventh International Conference (KR2000), Breckenridge, Colorado, April 12-15 2000

Model Checking Discounted Temporal Properties