

Arm Cortex M0 Workshop

This book explores how to develop STM32 Microcontroller programs with Arduino Sketch. Focusing on I/O development with various simple project demo. The following is a list of highlight topics in this book: * Preparing Development Environment * Sketch Programming * Working with Digital I/O * Working with Analog Input and PWM * Working with SPI * Working with I2C * Working with EEPROM * Working with DHT Module * Accessing a Network with Ethernet Module

This book constitutes revised papers from the Second International Workshop on IoT Streams for Data-Driven Predictive Maintenance, IoT Streams 2020, and First International Workshop on IoT, Edge, and Mobile for Embedded Machine Learning, ITEM 2020, co-located with ECML/PKDD 2020 and held in September 2020. Due to the COVID-19 pandemic the workshops were held online. The 21 full papers and 3 short papers presented in this volume were thoroughly reviewed and selected from 35 submissions and are organized according to the workshops and topics: IoT Streams 2020; Stream Learning; ITEM 2020; Unsupervised Machine Learning; Hardware Methods; Quantization.

This book constitutes revised selected papers from the 13th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2022, held in Leuven, Belgium, in April 2022. The 12 full papers presented in this volume were carefully reviewed and selected from 25 submissions. The papers cover the following subjects: implementation attacks, secure implementation, implementation attack-resilient architectures and schemes, secure design and evaluation, practical attacks, test platforms, and open benchmarks.

This book features the manuscripts accepted for the Special Issue ‘Applications in Electronics Pervading Industry, Environment and Society—Sensing Systems and Pervasive Intelligence’ of the MDDP Journal Sensors. Most of the papers come from a selection of the best papers of the 2019 edition of the ‘Applications in Electronics Pervading Industry, Environment and Society’ (APPLEPIES) Conference, which was held in November 2019. All these papers have been significantly enhanced with novel experimental results. The papers give an overview of the trends in research and development activities concerning the pervasive application of electronics in industry, the environment, and society. The focus of these papers is on cyber physical systems (CPS), with research proposals for new sensor acquisition and ADC (analog to digital converter) methods, high-speed communication systems, cybersecurity, big data management, and data processing including emerging machine learning techniques. Physical implementation aspects are discussed as well as the trade-off found between functional performance and hardware/systems costs.

16th International Workshop, Busan, South Korea, September 23-26, 2014, Proceedings
Proceedings, March 18–20, 1996, Pittsburgh, Pennsylvania
A Tutorial Approach

22nd International Conference, Seoul, South Korea, December 4–6, 2019, Revised Selected Papers
Designing Secure IoT Devices with the Arm Platform Security Architecture and Cortex-M33
Security of Industrial Control Systems and Cyber Physical Systems

Tessel 2 Development Workshop

This book offers the first comprehensive view on integrated circuit and system design for the Internet of Things (IoT), and in particular for the tiny nodes at its edge. The authors provide a fresh perspective on how the IoT will evolve based on recent and foreseeable trends in the semiconductor industry, highlighting the key challenges, as well as the opportunities for circuit and system innovation to address them. This book describes what the IoT really means from the design point of view, and how the constraints imposed by applications translate into integrated circuit requirements and design guidelines. Chapter contributions equally come from industry and academia. After providing a system perspective on IoT nodes, this book focuses on state-of-the-art design techniques for IoT applications, encompassing the fundamental sub-systems encountered in Systems on Chip for IoT: ultra-low power digital architectures and circuits-low and zero-leakage memories (including emerging technologies) circuits for hardware security and authentication System on Chip design methodologies on-chip power management and energy harvesting ultra-low power analog interfaces and analog-digital conversion short-range radios miniaturized battery technologies packaging and assembly of IoT integrated systems (on silicon and non-silicon substrates). As a common thread, all chapters conclude with a prospective view on the foreseeable evolution of the related technologies for IoT. The concepts developed throughout the book are exemplified by two IoT node system demonstrations from industry. The unique balance between breadth and depth of this book enables expert readers quickly to develop an understanding of the specific challenges and state-of-the-art solutions for IoT, as well as their evolution in the foreseeable future provides non-experts with a comprehensive introduction to integrated circuit design for IoT, and serves as an excellent starting point for further learning, thanks to the broad coverage of topics and selected references makes it very well suited for practicing engineers and scientists working in the hardware and chip design for IoT, and as textbook for senior undergraduate, graduate and postgraduate students (familiar with analog and digital circuits).

This book is a printed edition of the Special Issue ‘Raspberry Pi Technology’ that was published in Electronics

This book constitutes revised selected papers from the workshops held at 24th International Conference on Parallel and Distributed Computing, Euro-Par 2018, which took place in Turin, Italy, in August 2018. The 64 full papers presented in this volume were carefully reviewed and selected from 109 submissions. Euro-Par is an annual, international conference in Europe, covering all aspects of parallel and distributed processing. These range from theory to practice, from small to the largest parallel and distributed systems and infrastructures, from fundamental computational problems to full-fledged applications. From architecture, compiler, language and interface design and implementation to tools, support infrastructures, and application performance aspects.

This book enables readers to achieve ultra-low energy digital system performance. The author ’s main focus is the energy consumption of microcontroller architectures in digital (sub)-systems. The book covers a broad range of topics extensively: from circuits through design strategy to system architectures. The result is a set of techniques and a context to realize minimum energy digital systems. Several prototype silicon implementations are discussed, which put the proposed techniques to the test. The achieved results demonstrate an extraordinary combination of variation-resilience, high speed performance and ultra-low energy.

IEEE Workshop on Signal Processing Systems

Enabling the Internet of Things

17th International Workshop on Security, IWSEC 2022, Tokyo, Japan, August 31 – September 2, 2022, Proceedings

SparkFun Simblee BLE Development Workshop

Design and Architecture for Signal and Image Processing

SIPS ... Design and Implementation

ISCA 2010 International Workshops A4MMC, AMAS-BT, EAMA, WEED, WIOSCA, Saint-Malo, France, June 19-23, 2010, Revised Selected Papers

This book reports on the latest advances in the study of biomedical signal processing, and discusses in detail a number of open problems concerning clinical, biomedical and neural signals. It methodically collects and presents in a unified form the research findings previously scattered throughout various scientific journals and conference proceedings. In addition, the chapters are self-contained and can be read independently. Accordingly, the book will be of interest to university researchers, R&D engineers and graduate students who wish to learn the core principles of biomedical signal analysis, algorithms, and applications, while also offering a valuable reference work for biomedical engineers and clinicians who wish to learn more about the theory and recent applications of neural engineering and biomedical signal processing.

This book constitutes the refereed post-conference proceedings of the 5th International Conference on Cryptology and Information Security in Latin America, LATINCRYPT 2017, held in Havana, Cuba, in September 2017. The 29 papers presented were carefully reviewed and selected from 64 submissions. They are organized in the following topical sections: security protocols; public-key implementation; cryptanalysis; theory of symmetric-key cryptography; multiparty computation and privacy; new constructions; and adversarial cryptography.

This book constitutes revised selected papers from the 22nd International Conference on Information Security and Cryptology, ICISC 2019, held in Seoul, South Korea, in December 2019. The total of 18 papers presented in this volume were carefully reviewed and selected from 43 submissions. The papers were organized in topical sections named: public-key encryption and implementation; homomorphic encryption; secure multiparty computation; post-quantum cryptography; secret sharing and searchable encryption; storage security and information retrieval; and attacks and software security.

This book constitutes the refereed proceedings of the 17th International Workshop on Security, IWSEC 2022, which took place as a hybrid event in Tokyo, Japan, in August/September 2022. The 12 full papers presented in this book were carefully reviewed and selected from 34 submissions. They were organized in topical sections as follows: mathematical cryptography; system security and threat intelligence; symmetric-key cryptography; post-quantum cryptography; advanced cryptography.

Lightweight Cryptography for Security and Privacy

Proceedings : RSP 2021 : Darmstadt, Germany, July 1-3, 2002

Advances in Theory, Algorithms and Applications

Progress in Cryptology – LATINCRYPT 2017

12th International Workshop, COSADE 2021, Lugano, Switzerland, October 25–27, 2021, Proceedings

Raspberry Pi Technolgy

This book constitutes the refereed proceedings of seven workshops held at the 18th International Conference on Image Analysis and Processing, ICIAP 2015, in Genoa, Italy, in September 2015: International Workshop on Recent Advances in Digital Security: Biometrics and Forensics, BioFor 2015; International Workshop on Color in Texture and Material Recognition, CTMR 2015; International Workshop on Medical Imaging in Rheumatology: Advanced applications for the analysis of in ammaton and damage in the rheumatoid Joint, RHEUMA 2015; International Workshop on Image-Based Smart City Application, ISCA 2015; International Workshop on Multimedia Assisted Dietary Management, MADiMa 2015; International Workshop on Scene Background Modeling and Initialization, SBMI 2015; and International Workshop on Image and Video Processing for Quality of Multimedia Experience, QoEM 2015.

This book introduces Tessel 2 board development using JavaScript. Some experiments are provided to accelerate your learning. The following is a list of book topics that will be explored: * Preparing Development Environment * Setting Up Tessel 2 * Digital I/O and Interrupt * Serial Communication (UART) * PWM and Analog Input * Working with I2C * Working with SPI * Working with Tessel Network * Working with Microsoft Azure

This book constitutes the thoroughly refereed conference proceedings of the First International Workshop on Design and Architecture for Signal and Image Processing, DASIP 2022, held in Budapest, Hungary in June 2022. The 13 full included in the volume were carefully reviewed and selected from 32 submissions. They are organized in the following topical sections: leading signal, image and video processing and machine learning in custom embedded, edge and cloud computing architectures and systems.

The Designer’s Guide to the Cortex-M Family is a tutorial-based book giving the key concepts required to develop programs in C with a Cortex M- based processor. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling the engineer to easily develop basic C programs to run on the Cortex- M0/M0+/M3 and M4. It then examines the more advanced features of the Cortex architecture such as memory protection, operating modes and dual stack operation. Once a firm grounding in the Cortex M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS DSP library. With this book you will learn: The key differences between the Cortex M0/M0+/M3 and M4 How to write C programs to run on Cortex-M based processors How to make best use of the CoreSight debug system How to do RTOS development The Cortex-M operating modes and memory protection Advanced software techniques that can be used on Cortex-M microcontrollers How to optimise DSP code for the cortex M4 and how to build real time DSP systems An Introduction to the Cortex microcontroller software interface standard (CMSIS), a common framework for all Cortex M- based microcontrollers Coverage of the CMSIS DSP library for Cortex M3 and M4 An evaluation tool chain IDE and debugger which allows the accompanying example projects to be run in simulation on the PC or on low cost hardware

Biomedical Signal Processing

13th International Workshop, COSADE 2022, Leuven, Belgium, April 11-12, 2022, Proceedings

Energy-Efficient Distributed Computing Systems

9th International Workshop, CBCrypto 2021, Munich, Germany, June 21–22, 2021 Revised Selected Papers

6th International Conference, SPACE 2016, Hyderabad, India, December 14-18, 2016, Proceedings

IoT Streams for Data-Driven Predictive Maintenance and IoT, Edge, and Mobile for Embedded Machine Learning

First Workshop, CyberCS 2015 and First Workshop, WOS-CPS 2015 Vienna, Austria, September 21–22, 2015 Revised Selected Papers

This book constitutes the proceedings of the workshops of the 23rd International Conference on Parallel and Distributed Computing, Euro-Par 2016, held in Grenoble, France in August 2016. The 65 full papers presented were carefully reviewed and selected from 95 submissions. The volume includes the papers from the following workshops: Euro-EDUPAR (Second European Workshop on Parallel and Distributed Computing Education for Undergraduate Students) – HeteroPar 2016 (the 14th International Workshop on AI – IW MSE (5th International Workshop on Multicore Software Engineering) – LSDVE (Fourth Workshop on Large-Scale Distributed Virtual Environments) – PADABS (Fourth International Workshop on Parallel and Distributed Agent-Based Simulations) – PBio (Fourth International Workshop on Parallelism in Bioinformatics) – PELGA (Second Workshop on Performance Engineering for Large-Scale Graph Analytics) – Resilience (9th Workshop in Resilience in High Performance Computing) – ROME (Fourth Workshop on Runtime and Operating Systems for the Many-Core Era) – UCHPC (9th Workshop on UnConventional High-Performance Computing).

This book constitutes revised selected papers from the 9th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2018, held in Singapore, in April 2018.The 14 papers presented in this volume were carefully reviewed and selected from 31 submissions. They were organized in topical sections named: countermeasures against side-channel attacks: tools for side-channel analysis: fault attacks and hardware trojans: and side-channel analysis attacks.

This book constitutes the refereed proceedings of five International Workshops held as parallel events of the 18th IFIP WG 12.5 International Conference on Artificial Intelligence Applications and Innovations, AIAI 2022, virtually and in Hersonissos, Crete, Greece, in June 2022: the 11th Mining Humanistic Data Workshop (MHDW 2022): the 7th 5G-Putting Intelligence to the Network Edge Workshop (5G-PINE 2022): the 1st workshop on AI in Energy, Building and Micro-Grids (AIMBG 2022): the 1st Workshop/Special Session on Machine Learning and Big Data in Informatics (AIBEI 2022). The 35 full papers presented at these workshops were carefully reviewed and selected from 74 submissions.

This book constitutes revised selected papers from the 8th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2017, held in Paris, France, in April 2017. The 17 papers presented in this volume were carefully reviewed and selected from numerous submissions. They were organized in topical sections named: Side-Channel Attacks and Technological Effects: Side-Channel Countermeasures: Algorithmic Aspects in Side-Channel Attacks: Side-Channel Attacks: Fault Attacks: Embedded Security: and Side-Channel Tools.

This book constitutes the refereed proceedings of the First Conference on Cybersecurity of Industrial Control Systems, CyberICS 2015, and the First Workshop on the Security of Cyber Physical Systems, WOS-CPS 2015, held in Vienna, Austria, in September 2015 in conjunction with ESORICS 2015. The 6 revised full papers and 2 short papers of CyberICS 2015 presented together with 3 revised full papers of WOS-CPS 2015 were carefully reviewed and selected from 28 initial submission systems, plant control systems, engineering workstations, substitution equipment, programmable logic controllers, PLCs, and other industrial control system. WOS-CPS 2015 deals with the Security of Cyber Physical Systems, that exist everywhere around us, and range in size, complexity and criticality, from embedded systems used in smart vehicles, to SCADA systems in smart grids to control systems in water distribution systems, to smart transportation systems etc.

Embedded architecture co-synthesis and system integration / B. Lin, S. Vercauteren, and H. De Man -- A multi-level transformation approach to HW/SW codesign: a case study / T.K.-Y. Cheung, G. Hellestrand, and P. Kantamanon -- Fully parallel hardware/software codesign for multi-dimensional DSP applications / M. Sheligu, N.L. Passos, and E.H.-M. Sha -- A co-design methodology based on formal specification and high-level estimation / C. Carreras [and others] -- Speed-up estimation for HW/SW-systems / W. Hardt and W. Rosenstiel -- A framework for intracore estimation and granularity in HW/SW partitioning / J. Henkel and R. Ernst -- Partitioning and exploration strategies in the TOSCA co-design flow / A. Baloni, W. Fornaciari, and D. Scuto -- Process partitioning for distributed embedded systems / J. Hou and W. Wolf -- Two-level partitioning of image processing algorithms for the parallel map-oriented machine / R.W. Hartenstein, J. Becker, and R. Kress -- PACE: a dynamic programming algorithm for hardware/software partitioning / P.W. Knudsen and J. Madsen -- A model for the coanalysis of hardware and software systems / M. Gerndt -- Formal verification of embedded systems based on CFSM networks / F. Balain [and others] -- Towards a model for hardware and software functional partitioning / F. Valid and T. dm Le -- Implications of codesign as a natural constituent of a systems engineering discipline for computer-based systems / M. Voss and O. Hammerschmidt -- Uninterpreted co-simulation for performance evaluation of HW/SW systems / J.P. Calvez, D. Heller, and O. Pasquier -- Fast and accurate hardware-software co-simulation using software timing estimates / C. Passerone

Cryptographic Hardware and Embedded Systems -- CHES 2014

Euro-Par 2018: Parallel Processing Workshops

Euro-Par 2016: Parallel Processing Workshops

Applications in Electronics Pervading Industry, Environment and Society

STM32 Nucleo-32 Development Workshop

Constructive Side-Channel Analysis and Secure Design

Architecture of Computing Systems - ARCS 2012

This book constitutes the refereed proceedings of the 25th International Conference on Architecture of Computing Systems, ARCS 2012, held in Munich, Germany, in February/March 2012. The 20 revised full papers presented in 7 technical sessions were carefully reviewed and selected from 65 submissions. The papers are organized in topical sections on robustness and fault tolerance, power-aware processing, parallel processing, processor cores, optimization, and communication and memory.

STM32 Nucleo-32 Development WorkshopPE Press

This book was written to help anyone want to get started with STM32 Nucleo-32 board development. This book describes all the basic elements of the STM32 Nucleo-32 I/O development with step-by-step approach using GNU ARM, OpenOCD and mbed development. The following is a list of highlight topics in this book: * Preparing Development Environment * Setup Development Environment * Debugging * Digital Input/Output * Serial Communication - UART * Working with Analog Input (ADC) * Working with Analog Output (PWM) * Working with Analog Output (DAC) * Working with SPI * Working with I2C * mbed Development

This book constitutes the proceedings of the 16th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2014, held in Busan, South Korea, in September 2014. The 33 full papers included in this volume were carefully reviewed and selected from 127 submissions. They are organized in topical sections named: side-channel attacks; new attacks and constructions; countermeasures; algorithm specific SCA; ECC implementations; implementations; hardware implementations of symmetric cryptosystems; PUFs; and RNGs and SCA issues in hardware.

5th International Workshop, LightSec 2016, Aksaray, Turkey, September 21–22, 2016, Revised Selected Papers

20th International Conference, CARDIS 2021, Lübeck, Germany, November 11–12, 2021, Revised Selected Papers

New Trends in Image Analysis and Processing -- ICIAP 2015 Workshops

October 20-21, 1999, San Francisco, California

The Designer’s Guide to the Cortex-M Processor Family

Sensing Systems and Pervasive Intelligence

Security, Privacy, and Applied Cryptography Engineering

This book constitutes the refereed post-conference proceedings of the 5th International Workshop on Lightweight Cryptography for Security and Privacy, LightSec 2016, held in Aksaray, Turkey, in September 2016. The 9 full papers presented were carefully reviewed and selected from 18 submissions. The papers are organized in the following topical sections: cryptanalysis, lightweight designs, implementation challenges. The Designer’s Guide to the Cortex-M Microcontrollers gives you an easy-to-understand introduction to the concepts required to develop programs in C with a cortex-M based microcontroller. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling you to easily develop basic C programs to run on the Cortex-M0/M0+/M3 and M4 and M7. It then examines the more advanced features of the Cortex architecture such as memory protection, operating modes, and dual stack operation. Once a firm grounding in the Cortex-M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS-DSP library. The book also examines techniques for software testing and code reuse specific to Cortex-M microcontrollers. With this book you will learn: the key differences between the Cortex-M0/M0+/M3 and M4 and M7; how to write C programs to run on Cortex-M based processors; how to make the best use of the CoreSight debug system; the Cortex-M operating modes and memory protection; advanced software techniques that can be used on Cortex-M microcontrollers; how to use a Real Time Operating System with Cortex-M devices; how to optimize DSP code for the Cortex-M4; and how to build real time DSP systems. Includes an update to the latest version (5) of MDK-ARM, which introduces the concept of using software device packs and software components Includes overviews of the new CMSIS specifications Covers developing software with CMSIS-RTOS showing how to use RTOS in a real world design Provides a new chapter on the Cortex-M7 architecture covering all the new features Includes a new chapter covering test driven development for Cortex-M microcontrollers Features a new chapter on creating software components with CMSIS-Pack and device abstraction with CMSIS-Driver Features a new chapter providing an overview of the ARMv8-M architecture including the TrustZone hardware security model

Designing Secure IoT devices with the Arm Platform Security Architecture and Cortex-M33 explains how to design and deploy secure IoT devices based on the Cortex-M23/M33 processor. The book is split into three parts. First, it introduces the Cortex-M33 and its architectural design and major processor peripherals. Second, it shows how to design secure software and secure communications to minimize the threat of both hardware and software hacking. And finally, it examines common IoT cloud systems and how to design and deploy a fleet of IoT devices. Example projects are provided for the Keil MDK-ARM and NXP LPCpresso tool chains. Since their inception, microcontrollers have been designed as functional devices with a CPU, memory and peripherals that can be programmed to accomplish a huge range of tasks. With the growth of internet connected devices and the Internet of Things (IoT), plain old microcontrollers are no longer suitable as they lack the features necessary to create both a secure and functional device. The recent development by ARM of the Cortex M23 and M33 architecture is intended for today’s IoT world. Shows how to design secure software and secure communications using the ARM Cortex M23- and M33-based micro controllers Explains how to write secure code to minimize vulnerabilities using the CERT-C coding standard Uses the mbedTLS library to implement modern cryptography

This book constitutes the refereed proceedings of the 17th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2015, held in Saint Malo, France, in September 2015. The 34 full papers included in this volume were carefully reviewed and selected from 128 submissions. They are organized in the following topical sections: processing techniques in side-channel analysis; cryptographic hardware implementations; homomorphic encryption in hardware; side-channel attacks on public key cryptography; cipher design and cryptanalysis; true random number generators and entropy estimations; side-channel analysis and fault injection attacks; higher-order side-channel attacks; physically unclonable functions and hardware trojans; side-channel attacks in practice; and lattice-based implementations.

Euro-Par 2016 International Workshops, Grenoble, France, August 24–26, 2016, Revised Selected Papers

Arduino Sketch for STM32 Development Workshop

From Integrated Circuits to Integrated Systems

8th International Workshop, COSADE 2017, Paris, France, April 13–14, 2017, Revised Selected Papers

Cryptographic Hardware and Embedded Systems -- CHES 2015

25th International Conference, Munich, Germany, February 28 – March 2, 2012, Proceedings

9th International Workshop, COSADE 2018, Singapore, April 23–24, 2018, Proceedings

This book constitutes the thoroughly refereed post-conference proceedings of the workshops held at the 37th International Symposium on Computer Architecture, ISCA 2010, in Saint-Malo, France, in June 2010. The 28 revised full papers presented were carefully reviewed and selected from the lectures given at 5 of these workshops. The papers address topics ranging from novel memory architectures to emerging application design and performance analysis and encompassed the following workshops: A4MMC, applications for multi- and many-cores; A5-B, on-chip workshop on hardware and micro-architectural support for binary translation; EAMA, the 3rd Workshop for emerging applications and many-core architectures; WEED, 2nd Workshop on energy efficient design, as well as WIOSCA, the annual workshop on the interaction between operating systems and computer architecture.

Papers from a July 2002 workshop describe recent advances in reconfigurable control systems, system specification and modeling, distributed prototyping, efficient early evaluation, and prototyping methodologies and tools. Coverage includes prototyping applications ranging from FPGA-based embedded hardware to interactive software systems and networked communication systems; case studies; and topics such as rapid prototyping of FPGA-based floating point DSP systems, prototyping Ethernet in the first mile over point-to-point copper, interfacing software libraries from non-deterministic prototypes, reconfigurable hardware control software, and platform concepts for prototyping and demonstration of high-speed communication systems. This work lacks a subject index. Annotation copyrighted by Book News, Inc., Portland, OR.

This book constitutes revised selected papers from the 11th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2021, held in Lugano, Switzerland, in October 2021. The 14 full papers carefully reviewed and selected from 31 submissions are presented in this volume together with the 4 extended keynote abstracts. The workshop covers the following subjects: cryptography, side-channel analysis, cryptographic implementations, fault attacks, implementation attacks, post-quantum cryptography, hardware accelerators, etc.

Stellaris LM4F120 and Tiva C Series LaunchPad is great products based ARM Cortex-M for learning. This book helps you to get started with Stellaris LM4F120 and Tiva C Series LaunchPad and how to build programs using Energia and Code Composer Studio. The following is highlight topics: * Preparing Development Environment * Developing program using Energia * Developing program using Code Composer Studio 6.x * Accessing board through GPIO, Analog I/O, UART, I2C, and SPI * Providing several code samples to demonstrate how to work

13th International Workshop on Rapid System Prototyping

Advances in Information and Computer Security

Euro-Par 2018 International Workshops, Turin, Italy, August 27-28, 2018, Revised Selected Papers

ICIAP 2015 International Workshops, BioFor, CTMR, RHEUMA, ISCA, MADiMa, SBMI, and QoEM, Genoa, Italy, September 7-8, 2015, Proceedings

Code-Based Cryptography

5th International Conference on Cryptology and Information Security in Latin America, Havana, Cuba, September 20-22, 2017, Revised Selected Papers

Information Security and Cryptology – ICISC 2019