

### **Networks On Chips Elsevier**

*Software Defined Networks: A Comprehensive Approach, Second Edition provides in-depth coverage of the technologies collectively known as Software Defined Networking (SDN). The book shows how to explain to business decision-makers the benefits and risks in shifting parts of a network to the SDN model, when to integrate SDN technologies in a network, and how to develop or acquire SDN applications. In addition, the book emphasizes the parts of the technology that encourage opening up the network, providing treatment for alternative approaches to SDN that expand the definition of SDN as networking vendors adopt traits of SDN to their existing solutions. Since the first edition was published, the SDN market has matured, and is being gradually integrated and morphed into something more compatible with mainstream networking vendors. This book reflects these changes, with coverage of the OpenDaylight controller and its support for multiple southbound protocols, the Inclusion of NETCONF in discussions on controllers and devices, expanded coverage of NFV, and updated coverage of the latest approved version (1.5.1) of the OpenFlow specification. Contains expanded coverage of controllers Includes a new chapter on NETCONF and SDN Presents expanded coverage of SDN in optical networks Provides support materials for use in computer networking courses*

*The Handbook includes chapters on all the major industry standards, quick reference tables, helpful appendices, plus a new glossary and list of acronyms. This practical handbook can stand alone or as a companion volume to DeCusatis: Fiber Optic Data Communication: Technological Advances and Trends (February 2002, ISBN: 0-12-207892-6), which was*

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*developed in tandem with this book. \* Includes emerging technologies such as Infiniband, 10 Gigabit Ethernet, and MPLS Optical Switching \* Describes leading edge commercial products, including LEAF and MetroCore fibers, dense wavelength multiplexing, and Small Form Factor transceiver packages \* Covers all major industry standards, often written by the same people who designed the standards themselves \* Includes an expanded listing of references on the World Wide Web, plus hard-to-find references for international, homologation, and type approval requirements \* Convenient tables of key optical datacom parameters and glossary with hundreds of definitions and acronyms \* Industry buzzwords explained, including SAN, NAS, and MAN networking \* Datacom market analysis and future projections from industry leading forecasters*

*During the last three decades, reconfigurable logic has been growing steadily and can now be found in many different fields. Field programmable gate arrays (FPGAs) are one of the most famous architecture families of reconfigurable devices. FPGAs can be seen as arrays of logic units that can be reconfigured to realize any digital systems. Their high versatility has enabled designers to drastically reduce time to market, and made FPGAs suitable for prototyping or small production series in many branches of industrial products. In addition, and thanks to innovations at the architecture level, FPGAs are now conquering segments of mass markets such as mobile communications. Reconfigurable Logic: Architecture, Tools, and Applications offers a snapshot of the state of the art of reconfigurable logic systems. Covering a broad range of architectures, tools, and applications, this book: Explores classical FPGA architectures and their supporting tools Evaluates recent proposals related to FPGA architectures, including the use of network-on-chips (NoCs) Examines reconfigurable processors that merge concepts*

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*borrowed from the reconfigurable domain into processor design Exploits FPGAs for high-performance systems, efficient error correction codes, and high-bandwidth network routers with built-in security Expounds on emerging technologies to enhance FPGA architectures, improve routing structures, and create non-volatile configuration flip-flops Reconfigurable Logic: Architecture, Tools, and Applications reviews current trends in reconfigurable platforms, providing valuable insight into the future potential of reconfigurable systems.*

*Unlike books currently on the market, this book attempts to satisfy two goals: combine circuits and electronics into a single, unified treatment, and establish a strong connection with the contemporary world of digital systems. It will introduce a new way of looking not only at the treatment of circuits, but also at the treatment of introductory coursework in engineering in general. Using the concept of "abstraction," the book attempts to form a bridge between the world of physics and the world of large computer systems. In particular, it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems. Computer systems are simply one type of electrical systems. +Balances circuits theory with practical digital electronics applications. +Illustrates concepts with real devices. +Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach. +Written by two educators well known for their innovative teaching and research and their collaboration with industry. +Focuses on contemporary MOS technology.*

*Special Issue on On-chip Parallel and Network-based Systems*  
*Fueling the Next Information Revolution*

*Modeling Embedded Systems and SoC's*  
*Handbook of Fiber Optic Data Communication*  
*Silicon Photonics*

**Modeling and Simulation of Computer Networks and Systems: Methodologies and Applications** introduces you to a broad array of modeling and simulation issues related to computer networks and systems. It focuses on the theories, tools, applications and uses of modeling and simulation in order to effectively optimize networks. It describes methodologies for modeling and simulation of new generations of wireless and mobiles networks and cloud and grid computing systems. Drawing upon years of practical experience and using numerous examples and illustrative applications recognized experts in both academia and industry, discuss: Important and emerging topics in computer networks and systems including but not limited to; modeling, simulation, analysis and security of wireless and mobiles networks especially as they relate to next generation wireless networks Methodologies, strategies and tools, and strategies needed to build computer networks and systems modeling and simulation from the bottom up Different network performance metrics including, mobility, congestion, quality of service, security and more... Modeling and Simulation of Computer Networks and Systems is a must have resource for network architects, engineers and researchers who want to gain

**insight into optimizing network performance through the use of modeling and simulation. Discusses important and emerging topics in computer networks and Systems including but not limited to; modeling, simulation, analysis and security of wireless and mobiles networks especially as they relate to next generation wireless networks Provides the necessary methodologies, strategies and tools needed to build computer networks and systems modeling and simulation from the bottom up Includes comprehensive review and evaluation of simulation tools and methodologies and different network performance metrics including mobility, congestion, quality of service, security and more**

**ZigBee is a short-range wireless networking standard backed by such industry leaders as Motorola, Texas Instruments, Philips, Samsung, Siemens, Freescale, etc. It supports mesh networking, each node can transmit and receive data, offers high security and robustness, and is being rapidly adopted in industrial, control/monitoring, and medical applications. This book will explain the ZigBee protocol, discuss the design of ZigBee hardware, and describe how to design and implement ZigBee networks. The book has a dedicated website for the latest technical updates, ZigBee networking calculators, and additional materials. Dr. Farahani is a ZigBee system engineer for Freescale semiconductors Inc. The book comes with a dedicated website that contains additional resources and calculators:**

**<http://www.learnZigBee.com> Provides a comprehensive overview of ZigBee technology and networking, from RF/physical layer considerations to application layer development Discusses ZigBee security features such as encryption Describes how ZigBee can be used in location detection applications Explores techniques for ZigBee co-existence with other wireless technologies such as 802.11 and Bluetooth The book comes with a dedicated website that contains additional resources and calculators: <http://www.learnZigBee.com>**

**After nearly six years as the field's leading reference, the second edition of this award-winning handbook reemerges with completely updated content and a brand new format. The Computer Engineering Handbook, Second Edition is now offered as a set of two carefully focused books that together encompass all aspects of the field. In addition to complete updates throughout the book to reflect the latest issues in low-power design, embedded processors, and new standards, this edition includes a new section on computer memory and storage as well as several new chapters on such topics as semiconductor memory circuits, stream and wireless processors, and nonvolatile memory technologies and applications.**

**Architecture of Network Systems explains the practice and methodologies that will allow you to solve a broad range of problems in system design, including problems related to security, quality of service,**

**performance, manageability, and more. Leading researchers Dimitrios Serpanos and Tilman Wolf develop architectures for all network sub-systems, bridging the gap between operation and VLSI. This book provides comprehensive coverage of the technical aspects of network systems, including system-on-chip technologies, embedded protocol processing and high-performance, and low-power design. It develops a functional approach to network system architecture based on the OSI reference model, which is useful for practitioners at every level. It also covers both fundamentals and the latest developments in network systems architecture, including network-on-chip, network processors, algorithms for lookup and classification, and network systems for the next-generation Internet. The book is recommended for practicing engineers designing the architecture of network systems and graduate students in computer engineering and computer science studying network system design. This is the first book to provide comprehensive coverage of the technical aspects of network systems, including processing systems, hardware technologies, memory managers, software routers, and more. Develops a systematic approach to network architectures, based on the OSI reference model, that is useful for practitioners at every level. Covers both the important basics and cutting-edge topics in network systems architecture, including Quality of Service and Security for mobile, real-time P2P services, Low-Power Requirements**

**for Mobile Systems, and next generation Internet systems.**

**Theory and Practice**

**On-Chip Communication Architectures**

**Reconfigurable Logic**

**Issues in Networks Research and Application: 2013 Edition**

**ZigBee Wireless Networks and Transceivers**

**Handbook of Neural Computing Applications**

***The first book to survey this emerging field in digital system design.***

***Dark Silicon and the Future of On-chip Systems, Volume 110, the latest***

***release in the Advances in Computers series published since 1960,***

***presents detailed coverage of innovations in computer hardware,***

***software, theory, design and applications, with this release focusing on***

***an Introduction to dark silicon and future processors, a Revisiting of***

***processor allocation and application mapping in future CMPs in the dark***

***silicon era, Multi-objectivism in the dark silicon age, Dark silicon aware***

***resource management for many-core systems, Dynamic power***

***management for dark silicon multi-core processors, Topology***

***specialization for networks-on-chip in the dark silicon era, and Emerging***

***SRAM-based FPGA architectures. Provides in-depth surveys and tutorials***

***on new computer technology Covers well-known authors and researchers***

***in the field Presents extensive bibliographies with most chapters Includes***

***volumes that are devoted to single themes or subfields of computer***

***science, with this release focusing on Dark Silicon and Future On-chip Systems***

***Networks on Chips Technology and Tools Elsevier***

***Silicon photonics uses chip-making techniques to fabricate photonic circuits. The emerging technology is coming to market at a time of momentous change. The need of the Internet content providers to keep scaling their data centers is becoming increasingly challenging, the chip industry is facing a future without Moore's law, while telcos must contend with a looming capacity crunch due to continual traffic growth. Each of these developments is significant in its own right. Collectively, they require new thinking in the design of chips, optical components, and systems. Such change also signals new business opportunities and disruption. Notwithstanding challenges, silicon photonics' emergence is timely because it is the future of several industries. For the optical industry, the technology will allow designs to be tackled in new ways. For the chip industry, silicon photonics will become the way of scaling post-Moore's law. New system architectures enabled by silicon photonics will improve large-scale computing and optical communications. Silicon Photonics: Fueling the Next Information Revolution outlines the history and status of silicon photonics. The book discusses the trends driving the datacom and telecom industries, the main but not the only markets for silicon photonics. In particular, developments in optical transport and the***

***data center are discussed as are the challenges. The book details the many roles silicon photonics will play, from wide area networks down to the chip level. Silicon photonics is set to change the optical components and chip industries; this book explains how. Captures the latest research assessing silicon photonics development and prospects Demonstrates how silicon photonics addresses the challenges of managing bandwidth over distance and within systems Explores potential applications of SiP, including servers, datacenters, and Internet of Things***

***Optical Fiber Telecommunications VII  
From Implementations to Programming Paradigms  
RF Engineering for Wireless Networks  
Selected papers  
Computational Intelligence in Data Mining - Volume 3  
An Engineering Approach  
Networks-on-Chip: From Implementations to Programming Paradigms provides a thorough and bottom-up exploration of the whole NoC design space in a coherent and uniform fashion, from low-level router, buffer and topology implementations, to routing and flow control schemes, to co-optimizations of NoC and high-level programming paradigms. This textbook is intended for an advanced course on computer architecture, suitable for graduate students or***

***senior undergrads who want to specialize in the area of computer architecture and Networks-on-Chip. It is also intended for practitioners in the industry in the area of microprocessor design, especially the many-core processor design with a network-on-chip. Graduates can learn many practical and theoretical lessons from this course, and also can be motivated to delve further into the ideas and designs proposed in this book. Industrial engineers can refer to this book to make practical tradeoffs as well. Graduates and engineers who focus on off-chip network design can also refer to this book to achieve deadlock-free routing algorithm designs. Provides thorough and insightful exploration of NoC design space. Description from low-level logic implementations to co-optimizations of high-level program paradigms and NoCs. The coherent and uniform format offers readers a clear, quick and efficient exploration of NoC design space Covers many novel and exciting research ideas, which encourage researchers to further delve into these topics. Presents both engineering and theoretical contributions. The detailed description of the router, buffer and topology implementations, comparisons and analysis are of high engineering value.***

***Handbook of Neural Computing Applications is a collection of articles that deals with neural networks. Some papers review the biology of neural networks, their type and function (structure, dynamics, and learning) and compare a back-propagating perceptron with a Boltzmann machine, or a Hopfield network with a Brain-State-in-a-Box network. Other papers deal with specific neural network types, and also on selecting, configuring, and implementing neural networks. Other papers address specific applications including neurocontrol for the benefit of control engineers and for neural networks researchers. Other applications involve signal processing, spatio-temporal pattern recognition, medical diagnoses, fault diagnoses, robotics, business, data communications, data compression, and adaptive man-machine systems. One paper describes data compression and dimensionality reduction methods that have characteristics, such as high compression ratios to facilitate data storage, strong discrimination of novel data from baseline, rapid operation for software and hardware, as well as the ability to recognized loss of data during compression or reconstruction. The collection can prove helpful for programmers, computer engineers, computer technicians, and computer***

***instructors dealing with many aspects of computers related to programming, hardware interface, networking, engineering or design.***

***Modern electronics testing has a legacy of more than 40 years. The introduction of new technologies, especially nanometer technologies with 90nm or smaller geometry, has allowed the semiconductor industry to keep pace with the increased performance-capacity demands from consumers. As a result, semiconductor test costs have been growing steadily and typically amount to 40% of today's overall product cost. This book is a comprehensive guide to new VLSI Testing and Design-for-Testability techniques that will allow students, researchers, DFT practitioners, and VLSI designers to master quickly System-on-Chip Test architectures, for test debug and diagnosis of digital, memory, and analog/mixed-signal designs. Emphasizes VLSI Test principles and Design for Testability architectures, with numerous illustrations/examples. Most up-to-date coverage available, including Fault Tolerance, Low-Power Testing, Defect and Error Tolerance, Network-on-Chip (NOC) Testing, Software-Based Self-Testing, FPGA Testing, MEMS Testing, and System-In-Package (SIP) Testing, which are not yet available in***

***any testing book. Covers the entire spectrum of VLSI testing and DFT architectures, from digital and analog, to memory circuits, and fault diagnosis and self-repair from digital to memory circuits. Discusses future nanotechnology test trends and challenges facing the nanometer design era; promising nanotechnology test techniques, including Quantum-Dots, Cellular Automata, Carbon-Nanotubes, and Hybrid Semiconductor/Nanowire/Molecular Computing. Practical problems at the end of each chapter for students.***

***With optical fiber telecommunications firmly entrenched in the global information infrastructure, a key question for the future is how deeply will optical communications penetrate and complement other forms of communication (e.g., wireless access, on-premises networks, interconnects, and satellites). Optical Fiber Telecommunications, the seventh edition of the classic series that has chronicled the progress in the research and development of lightwave communications since 1979, examines present and future opportunities by presenting the latest advances on key topics such as: Fiber and 5G-wireless access networks Inter- and intra-data center communications Free-space and quantum communication***

***links Another key issue is the use of advanced photonics manufacturing and electronic signal processing to lower the cost of services and increase the system performance. To address this, the book covers: Foundry and software capabilities for widespread user access to photonic integrated circuits Nano- and microphotonic components Advanced and nonconventional data modulation formats The traditional emphasis of achieving higher data rates and longer transmission distances are also addressed through chapters on space-division-multiplexing, undersea cable systems, and efficient reconfigurable networking. This book is intended as an ideal reference suitable for university and industry researchers, graduate students, optical systems implementers, network operators, managers, and investors. Quotes: "This book series, which owes much of its distinguished history to the late Drs. Kaminow and Li, describes hot and growing applied topics, which include long-distance and wideband systems, data centers, 5G, wireless networks, foundry production of photonic integrated circuits, quantum communications, and AI/deep-learning. These subjects will be highly beneficial for industrial R&D engineers, university teachers and students, and funding agents in the business***

***sector." Prof. Kenichi Iga President (Retired), Tokyo Institute of Technology "With the passing of two luminaries, Ivan Kaminow and Tingye Li, I feared the loss of one of the premier reference books in the field. Happily, this new version comes to chronicle the current state-of-the-art and is written by the next generation of leaders. This is a must-have reference book for anyone working in or trying to understand the field of optical fiber communications technology." Dr. Donald B. Keck Vice President, Corning, Inc. (Retired) "This book is the seventh edition in the definitive series that was previously marshaled by the extraordinary Ivan Kaminow and Tingye Li, both sadly no longer with us. The series has charted the remarkable progress made in the field, and over a billion kilometers of optical fiber currently snake across the globe carrying ever-increasing Internet traffic. Anyone wondering about how we will cope with this incredible growth must read this book." Prof. Sir David Payne Director, Optoelectronics Research Centre, University of Southampton Updated edition presents the latest advances in optical fiber components, systems, subsystems and networks Written by leading authorities from academia and industry Gives a self-contained overview of specific technologies, covering both the***

***state-of-the-art and future research challenges***

***Special Issue on HW/SW Co-design: Systems and Networks on Chip  
Networks on Chips***

***Modeling and Simulation of Computer Networks and Systems***

***Bio-Inspired Fault-Tolerant Algorithms for Network-on-Chip  
Network Neuroscience***

***Design of Cost-Efficient Interconnect Processing Units***

***In 1994, W. Richard Stevens and Addison-Wesley published a networking classic: TCP/IP Illustrated. The model for that book was a brilliant, unfettered approach to networking concepts that has proven itself over time to be popular with readers of beginning to intermediate networking knowledge. The Illustrated Network takes this time-honored approach and modernizes it by creating not only a much larger and more complicated network, but also by incorporating all the networking advancements that have taken place since the mid-1990s, which are many. This book takes the popular Stevens approach and modernizes it, employing 2008 equipment, operating systems, and router vendors. It presents an ?illustrated? explanation of how TCP/IP works with consistent examples from a real, working***

***network configuration that includes servers, routers, and workstations. Diagnostic traces allow the reader to follow the discussion with unprecedented clarity and precision. True to the title of the book, there are 330+ diagrams and screen shots, as well as topology diagrams and a unique repeating chapter opening diagram. Illustrations are also used as end-of-chapter questions. A complete and modern network was assembled to write this book, with all the material coming from real objects connected and running on the network, not assumptions. Presents a real world networking scenario the way the reader sees them in a device-agnostic world. Doesn't preach one platform or the other. Here are ten key differences between the two: Stevens Goralski's Older operating systems (AIX,svr4,etc.) Newer OSs (XP, Linux, FreeBSD, etc.) Two routers (Cisco, Telebit (obsolete)) Two routers (M-series, J-series) Slow Ethernet and SLIP link Fast Ethernet, Gigabit Ethernet, and SONET/SDH links (modern) Tcpdump for traces Newer, better utility to capture traces (Ethereal, now has a new name!) No IPSec IPSec No multicast Multicast No router security discussed Firewall routers detailed No Web Full Web browser HTML consideration No IPv6 IPv6***

**overview Few configuration details More configuration details (ie, SSH, SSL, MPLS, ATM/FR consideration, wireless LANS, OSPF and BGP routing protocols New Modern Approach to Popular Topic Adopts the popular Stevens approach and modernizes it, giving the reader insights into the most up-to-date network equipment, operating systems, and router vendors. Shows and Tells Presents an illustrated explanation of how TCP/IP works with consistent examples from a real, working network configuration that includes servers, routers, and workstations, allowing the reader to follow the discussion with unprecedented clarity and precision. Over 330 Illustrations True to the title, there are 330 diagrams, screen shots, topology diagrams, and a unique repeating chapter opening diagram to reinforce concepts Based on Actual Networks A complete and modern network was assembled to write this book, with all the material coming from real objects connected and running on the network, bringing the real world, not theory, into sharp focus. Studying brain networks has become a truly interdisciplinary endeavor, attracting students and seasoned researchers alike from a wide variety of academic backgrounds. What has been lacking is an**

***introductory textbook that brings together the different fields and provides a gentle introduction to the major concepts and findings in the emerging field of network neuroscience. Network Neuroscience is a one-stop-shop that is of equal use to the neurobiologist, who is interested in understanding the quantitative methods employed in network neuroscience, and to the physicist or engineer, who is interested in neuroscience applications of mathematical and engineering tools. The book spans 27 chapters that cover everything from individual cells all the way to complex network disorders such as depression and autism spectrum disorders. An additional 12 toolboxes provide the necessary background for making network neuroscience accessible independent of the reader's background. Dr. Flavio Frohlich ([www.networkneuroscientist.org](http://www.networkneuroscientist.org)) wrote this book based on his experience of mentoring dozens of trainees in the Frohlich Lab, from undergraduate students to senior researchers. The Frohlich lab ([www.frohlichlab.org](http://www.frohlichlab.org)) pursues a unique and integrated vision that combines computer simulations, animal model studies, human studies, and clinical trials with the goal of developing novel brain stimulation treatments for psychiatric disorders. The book is based on***

***a course he teaches at UNC that has attracted trainees from many different departments, including neuroscience, biomedical engineering, psychology, cell biology, physiology, neurology, and psychiatry. Dr. Frohlich has consistently received rave reviews for his teaching. With this book he hopes to make his integrated view of neuroscience available to trainees and researchers on a global scale. His goal is to make the book the training manual for the next generation of (network) neuroscientists, who will be fusing biology, engineering, and medicine to unravel the big questions about the brain and to revolutionize psychiatry and neurology. Easy-to-read, comprehensive introduction to the emerging field of network neuroscience Includes 27 chapters packed with information on topics from single neurons to complex network disorders such as depression and autism Features 12 toolboxes serve as primers to provide essential background knowledge in the fields of biology, mathematics, engineering, and physics One of the greatest challenges faced by designers of digital systems is optimizing the communication and interconnection between system components. Interconnection networks offer an attractive and***

***economical solution to this communication crisis and are fast becoming pervasive in digital systems. Current trends suggest that this communication bottleneck will be even more problematic when designing future generations of machines. Consequently, the anatomy of an interconnection network router and science of interconnection network design will only grow in importance in the coming years. This book offers a detailed and comprehensive presentation of the basic principles of interconnection network design, clearly illustrating them with numerous examples, chapter exercises, and case studies. It incorporates hardware-level descriptions of concepts, allowing a designer to see all the steps of the process from abstract design to concrete implementation. Case studies throughout the book draw on extensive author experience in designing interconnection networks over a period of more than twenty years, providing real world examples of what works, and what doesn't. Tightly couples concepts with implementation costs to facilitate a deeper understanding of the tradeoffs in the design of a practical network. A set of examples and exercises in every chapter help the reader to fully understand all the implications of every design decision.***

***New design architectures in computer systems have surpassed industry expectations. Limits, which were once thought of as fundamental, have now been broken. Digital Systems and Applications details these innovations in systems design as well as cutting-edge applications that are emerging to take advantage of the fields increasingly sophisticated capabilities. This book features new chapters on parallelizing iterative heuristics, stream and wireless processors, and lightweight embedded systems. This fundamental text— Provides a clear focus on computer systems, architecture, and applications Takes a top-level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor, VLIW architecture, as well as new trends in multithreading and multiprocessing. includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications, circuit implementation aspects, parallel I/O algorithms, and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and***

***performance-enhancing techniques such as branch prediction, register renaming, and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives***

***The Computer Engineering Handbook***

***Architecture of Network Systems***

***Electrokinetics in Microfluidics***

***Proceedings of the International Conference on CIDM, 20-21***

***December 2014***

***How TCP/IP Works in a Modern Network***

***The Illustrated Network***

VLSI 2010 Annual Symposium will present extended versions of the best papers presented in ISVLSI 2010 conference. The areas covered by the papers will include among others: Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing. MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis,

Electrical/Packaging Co-Design, Physical Design, Intellectual property creating and sharing.

Over the past decade, system-on-chip (SoC) designs have evolved to address the ever increasing complexity of applications, fueled by the era of digital convergence. Improvements in process technology have effectively shrunk board-level components so they can be integrated on a single chip. New on-chip communication architectures have been designed to support all inter-component communication in a SoC design. These communication architecture fabrics have a critical impact on the power consumption, performance, cost and design cycle time of modern SoC designs. As application complexity strains the communication backbone of SoC designs, academic and industrial R&D efforts and dollars are increasingly focused on communication architecture design. On-Chip Communication Architectures is a comprehensive reference on concepts, research and trends in on-chip communication architecture design. It will provide readers with a comprehensive survey, not available elsewhere, of all current standards for on-chip communication architectures. A definitive guide to on-chip communication architectures, explaining key concepts, surveying research efforts and predicting future trends Detailed analysis of all popular standards for on-chip communication architectures Comprehensive survey of all research on

communication architectures, covering a wide range of topics relevant to this area, spanning the past several years, and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

System level design is a critical component for the methods to develop designs more productively. But there are a number of challenges in implementing system level modeling. This book addresses that need by developing organizing principles for understanding, assessing, and comparing the different models of computation in system level modeling.

The contributed volume aims to explicate and address the difficulties and challenges for the seamless integration of two core disciplines of computer science, i.e., computational intelligence and data mining. Data Mining aims at the automatic discovery of underlying non-trivial knowledge from datasets by applying intelligent analysis techniques. The interest in this research area has experienced a considerable growth in the last years due to two key factors: (a) knowledge hidden in organizations' databases can be exploited to improve strategic and managerial decision-making; (b) the large volume of data managed by organizations makes it impossible to carry out a manual analysis. The book addresses different methods and techniques of integration for enhancing the

overall goal of data mining. The book helps to disseminate the knowledge about some innovative, active research directions in the field of data mining, machine and computational intelligence, along with some current issues and applications of related topics.

Technology and Tools

Digital Systems and Applications

Principles and Practices of Interconnection Networks

Nanometer Design for Testability

Multiprocessor Systems-on-chips

Special Issue: System and Network on Chip

Cloud Computing: Theory and Practice provides students and IT professionals with an in-depth analysis of the cloud from the ground up. Beginning with a discussion of parallel computing and architectures and distributed systems, the book turns to contemporary cloud infrastructures, how they are being deployed at leading companies such as Amazon, Google and Apple, and how they can be applied in fields such as healthcare, banking and science. The volume also examines how to successfully deploy a cloud application across the enterprise using virtualization, resource management and the right amount of networking support, including content delivery networks and storage area networks.

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Developers will find a complete introduction to application development provided on a variety of platforms. Learn about recent trends in cloud computing in critical areas such as: resource management, security, energy consumption, ethics, and complex systems Get a detailed hands-on set of practical recipes that help simplify the deployment of a cloud based system for practical use of computing clouds along with an in-depth discussion of several projects Understand the evolution of cloud computing and why the cloud computing paradigm has a better chance to succeed than previous efforts in large-scale distributed computing

Foreword -- Foreword to the First Printing -- Preface -- Chapter 1 -- Introduction -- Chapter 2 -- Message Switching Layer -- Chapter 3 -- Deadlock, Livelock, and Starvation -- Chapter 4 -- Routing Algorithms -- Chapter 5 -- CollectiveCommunicationSupport -- Chapter 6 -- Fault-Tolerant Routing -- Chapter 7 -- Network Architectures -- Chapter 8 -- Messaging Layer Software -- Chapter 9 -- Performance Evaluation -- Appendix A -- Formal Definitions for Deadlock Avoidance -- Appendix B -- Acronyms -- References -- Index.

Network on Chip (NoC) addresses the communication requirement of different nodes on System on Chip. The bio-inspired algorithms improve the bandwidth utilization, maximize the throughput and reduce the end-to-end latency and inter-flit arrival time. This book exclusively presents in-depth information regarding bio-

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inspired algorithms solving real world problems focussing on fault-tolerant algorithms inspired by the biological brain and implemented on NoC. It further documents the bio-inspired algorithms in general and more specifically, in the design of NoC. It gives an exhaustive review and analysis of the NoC architectures developed during the last decade according to various parameters. Key Features: Covers bio-inspired solutions pertaining to Network-on-Chip (NoC) design solving real world examples Includes bio-inspired NoC fault-tolerant algorithms with detail coding examples Lists fault-tolerant algorithms with detailed examples Reviews basic concepts of NoC Discusses NoC architectures developed-to-date

A presentation of state-of-the-art approaches from an industrial applications perspective, Communication Architectures for Systems-on-Chip shows professionals, researchers, and students how to attack the problem of data communication in the manufacture of SoC architectures. With its lucid illustration of current trends and research improving the performance, quality, and reliability of transactions, this is an essential reference for anyone dealing with communication mechanisms for embedded systems, systems-on-chip, and multiprocessor architectures—or trying to overcome existing limitations. Exploring architectures currently implemented in manufactured SoCs—and those being

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proposed—this book analyzes a wide range of applications, including: Well-established communication buses Less common networks-on-chip Modern technologies that include the use of carbon nanotubes (CNTs) Optical links used to speed up data transfer and boost both security and quality of service (QoS) The book 's contributors pay special attention to newer problems, including how to protect transactions of critical on-chip information (personal data, security keys, etc.) from an external attack. They examine mechanisms, revise communication protocols involved, and analyze overall impact on system performance.

Interconnection Networks

Computational Intelligence in Pattern Recognition

Networks-on-Chip

Concurrency and Time in Models of Computation

VLSI 2010 Annual Symposium

Hardware, Antennas, and Propagation

**Finally, here is a single volume containing all of the engineering information needed to successfully design and implement any type of wireless network! Author Dan Dobkin covers every aspect of RF engineering necessary for wireless networks. He begins with a review of essential math and electromagnetic theory followed by thorough**

**discussions of multiplexing, modulation types, bandwidth, link budgets, network concepts, radio system architectures, RF amplifiers, mixers and frequency conversion, filters, single-chip radio systems, antenna theory and designs, signal propagation, as well as planning and implementing wireless networks for both indoor and outdoor environments. The appendices contain such vital data as U.S., European, and Japanese technical and regulatory standards for wireless networks, measurements in wireless networks, reflection and matching of transmission lines, determining power density, and much more. No matter what type of wireless network you design—Bluetooth, UWB, or even metropolitan area network (MAN)—this book is the one reference you can't do without! The A-to-Z guide to wireless network engineering—covers everything from basic electromagnetic theory to modulation techniques to network planning and implementation! Engineering and design principles covered are applicable to any type of wireless network, including 802.11, 802.16, 802.20, and Bluetooth. Discusses state-of-the-art modulation techniques such as ultra wideband (UWB) and orthogonal frequency-division multiplexing (OFDM). As the demand for digital communication networks has increased, so have the challenges in network component design. To meet ever-escalating**

**performance, flexibility, and economy requirements, the networking industry has opted to build products around network processors. These new chips range from task-specific processors, such as classification and encryption engines, to more general-purpose packet or communications processors. Programmable yet application-specific, their designs are tailored to efficiently implement communications applications such as routing, protocol analysis, voice and data convergence, firewalls, VPNs, and QoS. Network processor design is an emerging field with issues and opportunities both numerous and formidable. To help meet this challenge, the editors of this volume created the first Workshop on Network Processors, a forum for scientists and engineers from academia and industry to discuss their latest research in the architecture, design, programming, and use of these devices. In addition to including the results of the Workshop in this volume, the editors also present specially commissioned material from practicing designers, who discuss their companies' latest network processors. Network Processor Design: Issues and Practices is an essential reference on network processors for graduate students, researchers, and practicing designers. \* Includes contributions from major academic and industrial research labs including Aachen**

**University of Technology; Cisco Systems; Infineon Technologies; Intel Corp.; North Carolina State University; Swiss Federal Institute of Technology; University of California, Berkeley; University of Dortmund; University of Washington; and Washington University. \* Examines the latest network processors from Agere Systems, Cisco, IBM, Intel, Motorola, Sierra Inc., and TranSwitch.**

**A lab-on-a-chip device is a microscale laboratory on a credit-card sized glass or plastic chip with a network of microchannels, electrodes, sensors and electronic circuits. These labs on a chip can duplicate the specialized functions as performed by their room-sized counterparts, such as clinical diagnoses, PCR and electrophoretic separation. The advantages of these labs on a chip include significant reduction in the amounts of samples and reagents, very short reaction and analysis time, high throughput and portability. Generally, a lab-on-a-chip device must perform a number of microfluidic functions: pumping, mixing, thermal cycling/incubating, dispensing, and separating. Precise manipulation of these microfluidic processes is key to the operation and performance of labs on a chip. The objective of this book is to provide a fundamental understanding of the interfacial electrokinetic phenomena in several key microfluidic processes,**

and to show how these phenomena can be utilised to control the microfluidic processes. For this purpose, this book emphasises the theoretical modelling and the numerical simulation of these electrokinetic phenomena in microfluidics. However, experimental studies of the electrokinetic microfluidic processes are also highlighted in sufficient detail. The first book which systematically reviews electrokinetic microfluidics processes for lab-on-a chip applications Covers modelling and numerical simulation of the electrokinetic microfluidics processes Providing information on experimental studies and details of experimental techniques, which are essential for those who are new to this field Issues in Networks Research and Application: 2013 Edition is a ScholarlyEditions™ book that delivers timely, authoritative, and comprehensive information about Communication Networks. The editors have built Issues in Networks Research and Application: 2013 Edition on the vast information databases of ScholarlyNews.™ You can expect the information about Communication Networks in this book to be deeper than what you can access anywhere else, as well as consistently reliable, authoritative, informed, and relevant. The content of Issues in Networks Research and Application: 2013 Edition has been produced by the world's

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**A Comprehensive Approach**

**System-on-Chip Test Architectures**

**Special Issue on Network-on-chip Architectures and Design Methodologies  
Architecture, Tools, and Applications**

**Methodologies and Applications**

**Cloud Computing**

**This book presents practical development experiences in different areas of data analysis and pattern recognition, focusing on soft computing technologies, clustering and classification algorithms, rough set and fuzzy set theory, evolutionary computations, neural science and neural network systems, image processing, combinatorial pattern matching, social network analysis, audio and video data analysis, data mining in dynamic environments, bioinformatics, hybrid computing, big data analytics and deep learning. It also provides**

innovative solutions to the challenges in these areas and discusses recent developments.

**Streamlined Design Solutions Specifically for NoC** To solve critical network-on-chip (NoC) architecture and design problems related to structure, performance and modularity, engineers generally rely on guidance from the abundance of literature about better-understood system-level interconnection networks. However, on-chip networks present several distinct challenges that require novel and specialized solutions not found in the tried-and-true system-level techniques. **A Balanced Analysis of NoC Architecture** As the first detailed description of the commercial Spidergon STNoC architecture, **Design of Cost-Efficient Interconnect Processing Units: Spidergon STNoC** examines the highly regarded, cost-cutting technology that is set to replace well-known shared bus architectures, such as STBus, for demanding multiprocessor system-on-chip (SoC) applications. Employing a balanced, well-organized structure, simple teaching methods, numerous illustrations, and easy-to-understand examples, the authors explain: how the SoC and NoC technology works why developers designed it the way they did the system-level design methodology and tools used to configure the Spidergon STNoC architecture differences in cost structure between NoCs and system-level networks From professionals in computer sciences, electrical engineering, and other

related fields, to semiconductor vendors and investors – all readers will appreciate the encyclopedic treatment of background NoC information ranging from CMPs to the basics of interconnection networks. The text introduces innovative system-level design methodology and tools for efficient design space exploration and topology selection. It also provides a wealth of key theoretical and practical MPSoC and NoC topics, such as technological deep sub-micron effects, homogeneous and heterogeneous processor architectures, multicore SoC, interconnect processing units, generic NoC components, and embeddings of common communication patterns.

The design of today's semiconductor chips for various applications, such as telecommunications, poses various challenges due to the complexity of these systems. These highly complex systems-on-chips demand new approaches to connect and manage the communication between on-chip processing and storage components and networks on chips (NoCs) provide a powerful solution. This book is the first to provide a unified overview of NoC technology. It includes in-depth analysis of all the on-chip communication challenges, from physical wiring implementation up to software architecture, and a complete classification of their various Network-on-Chip approaches and solutions. \* Leading-edge research from world-renowned experts in academia and industry with state-of-the-art technology

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**implementations/trends \* An integrated presentation not currently available in any other book \* A thorough introduction to current design methodologies and chips designed with NoCs**

**System on Chip Interconnect**

**Issues and Practices**

**Dark Silicon and Future On-chip Systems**

**Foundations of Analog and Digital Electronic Circuits**

**Software Defined Networks**

**Network Processor Design**